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Press Release

VLSI Design Conference 2023 Begins on High Note with Discussions on Disruptive Semiconductor Innovations and Future Opportunities

• Shri. Rajeev Chandrasekhar, MoS Electronics & Information Technology and Skill Development & Entrepreneurship Inaugurates – VLSI Design Conference 2023 in Hyderabad.
• Revolutionizing the Semiconductor Industry: Exploring New Horizons in CMOS Design and Beyond Silicon Solutions, Spotlight on Next-Generation Materials and High-Level Design Techniques.
• More than 2000 delegates are attending the 5-day event
• Highlights at the event include Innovation Exhibits, Keynotes, Technical Sessions, Startup-Investor connect and the Innovator Awards.

Hyderabad, India, January 10, 2023: The VLSI Design Conference 2023 got off to a great start today with motivating speeches from industry leaders and discussions about the newest advancements in VLSI, embedded technologies, and opportunities for innovation.

The 5-day conference being held in Hyderabad from 8th to 12th January themed "Semiconductors Driving Disruptive Innovations in Global Digitalization," promises to be a thought-provoking event for attendees.

The event is designed to bring together key stakeholders, and was well represented by engineers, students and faculty, industry professionals, academia, researchers, bureaucrats, and government organizations.

Addressing the attendees on the inaugural session Shri. Rajeev Chandrasekhar said "The Ministry of Electronics and Information Technology has a program called the Future Design Program, which invests 200 million dollars in startups that design code, IP, tools, or devices for the next generation of applications in India. To support this innovation ecosystem, the government will soon launch an IT Server and IT hardware PLI. The IT PLI will also offer additional incentives for manufacturers that incorporate Indian-designed IP into their products. Further, the government is also focused on Digital India RISC-V (DIR-V) program for next generation Microprocessors to achieve commercial silicon & Design wins. We are putting Government capital to work actively, catalysing the Indian semiconductor ecosystem and working with industry and the global diaspora to achieve India's ambition as a semiconductor nation at a torrid pace."

Shri Jayesh Ranjan, Principal Secretary of Information Technology, Industry and Commerce for the government of Telangana in his address said, "The state is at the forefront of the startup revolution and has already created a wave of opportunities for innovation and startups across

the technology sector and VLSI field, with leading semiconductor companies having their base, and rapidly expanding in Hyderabad."

Among the esteemed dignitaries on the dais were Shri Jayesh Ranjan, Principal Secretary of Information Technology, Industry and Commerce for the government of Telangana; Victor Peng, President of AMD; Dr. Satya Gupta, President of the VLSI Society of India; Shashi Reddy, Vice President and Country Head of Qualcomm; Jaya Jagadish, Country Head and Senior Vice President of AMD India; and Venkata Simhadri, MD and CEO of MosChip.

In his address, Dr. Satya Gupta, the President of VLSI Society of India highlighted the role of VLSI in developing and accelerating the pace of the sector in India and how it shaped the growth of startups. With scaling reaching its fundamental limits, he said it is time to look at addressing technological challenges at higher levels of abstraction in CMOS-based design and at the same time, look beyond Silicon for further performance enhancement.

The semiconductor industry is poised for monumental growth with the Government of India having earmarked USD 10 billion for India Semiconductor Mission, creating an environment that encourages growth to create a vibrant semiconductor ecosystem and steer India into the future.
The Ministers also highlighted Governments' partnerships with the industry by reiterating commitment and a keen interest to work with the stakeholders to realize its efforts toward actualizing new milestones.

Venkata Simhadri, General Chair, VLSID Conference 2023, MD and CEO of MosChip, gave vote thanks at the end of the inaugural session, thanking the chief guests, guest of honour, and the attendees for their contribution and participation.

Keynote address trends, and the way forward for the VLSI, Embedded Industry

Following the inaugural, the industry leaders participated in keynote speeches and panel discussions, providing valuable insights and perspectives on the latest advancements, challenges, and trends in the field of VLSI and embedded technology.

Victor Peng, President of AMD, in his keynote discussed the importance of heterogeneous, high-performance, and adaptive computing in a digital world characterized by massive growth in data and the need to turn insights into intelligence using pervasive AI.

Assaf Touboul, VP of Technology at Qualcomm, provided an overview of key use cases being tackled by 5G and the role of semiconductors and AI/ML in improving the technology.

Dr. Prith Banerjee, CTO of Ansys, talked about the role of simulation in driving product innovation in the semiconductor and electronics industries. The conference also featured tracks on technical topics, user design, and industry forum talks focused on 5G.

Purdue's Prof Koushik Roy spoke on "Rethinking Computing with Neuro-inspired Learning: Algorithms, Circuits, and Devices." Deep learning has helped computers match or surpass human performance in vision, voice, and NLP. Implementing neural algorithms in typical "von-Neumann" architectures is much more expensive than in the biological brain.

Venkat Mattela, CEO of Ceremorphic, discussed AI-era semiconductor challenges and prospects. AI and ML have spurred semiconductor growth as microprocessors did decades

ago. This expansion has posed additional dependability, energy, and security problems. This session will examine its complexity and engineering prospects.

Following the keynote were 4 tracks, 2 tracks on technical, 1 track on the User Design Track, and 1 on " "5G: Opportunities and Challenges"

Startups and Investor Connect and other highlights of VLSID 2023

The "Startups Investor Connect" will be the key highlight of the conference on the 12th of January 2023, where emerging startups in the VLSI and embedded sector will get an opportunity to pitch their ideas to investors.

Participating startups included Anew Design Automation, Lightspeed Photonics, Vasbeam, Cyrrup, and more. Leading investors will be in attendance to hear pitches and consider potential investments. An award ceremony will be held to recognize the best startup at the conference.

The exhibition at the conference featured notable VLSI companies and innovative technology startups, highlighting the current capabilities in the sector and exciting opportunities for innovation. The 5-day conference will conclude on the 12th of January 2023 with keynote speeches, exhibits, startup pitching and product launches.

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Image caption

Dignitaries on the dais during Inaugural and MoS address to the attendees

Product Launch: Shri Jayesh Ranjan, Principal IT and (I&C) Secretary, Govt. of Telangana launches India's first 65 W GaN Charger made by POWERICS, partnered by Transphorm, Inc., MosChip & Silanna Semiconductors at the Exhibition area launch of 36th International VLSI Design Conference.

Reportage

THE TIMES OF INDIA

Edition: Print & Online | Page: National	Date: 10th January 2023

Centre to roll out IT hardware, PLI, sops for India-designed IP

TIMES NEWS NETWORK

Hyderabad: The Centre is gearing up to roll out a production linked incentive (PLI) scheme for IT hardware and IT servers with additional incentives for original equipment manufacturers (OEMs) and manufacturers incorporating India-designed IP in their systems and products, Union minister of state for electronics & information technology (MeitY) Rajeev Chandrasekhar said.

Inaugurating virtually the 36th edition of the international conference on VLSI Design 2023 and the 22nd international conference on embedded systems being held in Hyderabad, Chandrasekhar said the PLI would be similar to the one for mobile phones which had been extremely successful.

He said the soon-to-be-launched PLI scheme was part of the government's efforts to re-architect and diversify the innovation and tech ecosystem to make India's `Techade' a reality.

Pointing out that the government capital is being to work in actively catalysing the country's semiconductor ecosystem, he said the government is working at a torrent pace to achieve in the next five years what most countries like China took decades to achieve.

He cited the instance of the Future Design Programme, which will invest $200 million in startups that design or co-design IP, tools or devices for next generation of applications in India, he said: "By 2024, we believe India would have stepped into the semiconductor manufacturing space and catalysed a more domestic design and innovation ecosystem where we are encouraging startups to work with leading global majors to develop IP and devices that are either co-owned or owned."

India's Techade is not just about the future of the internet or direct to consumer technology but just as much about electronics and semiconductors, Rajeev Chandrasekhar said.

WHAT'S PLI SCHEME FOR HARDWARE

Scheme part of govt efforts to re-architect & diversify tech ecosystem to make India's 'Techade' a reality

- Govt capital to actively catalyse country's semiconductor ecosystem
- PLI planned to ensure these devices & innovation ecosystem fully supported by market
- India's Techade not just about future of internet or direct to consumer tech but as much about electronics & semiconductors, says Union minister Rajeev Chandrasekhar

> "By 2024, we believe India would have stepped into the semiconductor manufacturing space and catalysed a more domestic design and innovation ecosystem where we are encouraging startups to work with leading global majors to develop IP and devices that are either co-owned or owned —**Rajeev Chandrasekhar** | MEITY MINISTER

THE HANS INDIA

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Startups to pitch ideas at VLSI Design meet

HYDERABAD: Startups Investor Connect will be the key highlight of VLSI Design Conference 2023 on January 12, 2023, the last day of the event. The emerging startups in the very-large-scale integration (VLSI) and embedded sector will get an opportunity to pitch their ideas to investors. Anew Design Automation, Lightspeed Photonics, Vasbeam and Cyrrup are some of the startups participating in the programme. Leading investors will be in attendance to hear the pitches and consider potential investments. The VLSI Design Conference is underway in Hyderabad from January 8, 2023 with a theme 'Semiconductors Driving Disruptive Innovations in Global Digitalization'. The five-day event is designed to bring together key stakeholders.

DECCAN Chronicle

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Union IT ministry announces $200 million investment in start-ups

DECCAN CHRONICLE. | DC CORRESPONDENT

MD & CEO, MosChip, Venkata Simhadri; VLSI Society of India president Dr Satya Gupta; senior vice-president, AMD India, Jaya Jagadish; vice-president (engineering), Qualcomm Shashi Reddy; IT principal secretary Jayesh Ranjan and AECG-AMD president Victor Peng, at the inauguration of VLSI Design Conference on Tuesday. (Photo By Arrangement)

HYDERABAD: Union minister of state for electronics and information technology Rajeev Chandrasekhar announced a $200 million investment in start-ups that design code, IP, tools, or devices, claiming that by 2024 India would have entered the semiconductor manufacturing space and catalysed a larger, more domestic design and ecosystem.

While speaking at 'Very Large Scale Integration (VLSI) Design Conference' on the theme 'Semiconductors Driving Disruptive Innovations in Global Digitalisation', he stated that the Centre would launch a production linked incentive (PLI) scheme for IT server and hardware manufacturing.

The VLSI event, which will be attended by over 2,000 delegates, saw several stakeholders speak on advancements in VLSI, embedded technologies, and opportunities for innovation.

"We believe by 2024, India would have stepped into the semiconductor manufacturing space and catalysed a larger, more domestic design and ecosystem where we encourage start-ups to work with leading global majors to develop IP and devices, for which the government has announced the Future Design Program, through which $200 million will be invested in start-ups that design code, IP, tools, or devices for the next generation of applications in India," he stated.

He stated that global trends such as increased demand for talent and redesigned supply chains were driving unprecedented growth for digital products and services, and that India's objectives as a semiconductor nation and electronic powerhouse were relevant and significant.

IT principal secretary Jayesh Ranjan said Telangana had created several opportunities for innovation and start-ups across the technology sector and VLSI field, with leading semiconductor companies having their base, and rapidly expanding in Hyderabad.

Dr Satya Gupta, president of the VLSI Society of India, remarked that it was necessary to look at addressing technological issues at higher levels of abstraction in CMOS-based design while also looking beyond Silicon for performance enhancement.

THE HINDU BusinessLine

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PLI for IT servers, hardware on the anvil: Rajeev Chandrasekhar

Minister of State for Electronics and IT Rajeev Chandrasekhar

Our Bureau
Hyderabad

After launching PLI (Production Linked Incentives) for several sectors, the Union Government has announced that it is going to launch an exclusive PLI for IT Servers and IT Hardware.

Addressing the inaugural of VLSI Design Conference 2023 virtually on Tuesday, Union Minister of State for Electronics and Information Technology and Skill Development Rajeev Chandrasekhar said that the upcoming PLI would promote innovation ecosystem and product development in the country.

"The IT PLI will also offer additional incentives for manufacturers that incorporate Indian-designed IP into their products," he said.

The government is also focussed on Digital India RISC-V (DIR-V) programme for next generation microprocessors to achieve commercial silicon and design wins.

THRUST ON SEMICON

"We are putting Government capital to catalyse the Indian semiconductor ecosystem and working with industry and the global diaspora to achieve India's ambition as a semiconductor nation at a torrid pace," he said. The five-day conference, with the theme 'Semiconductors Driving Disruptive Innovations in Global Digitalisation', is being attended by industry leaders and other stakeholders.

Victor Peng, President of AMD, discussed the importance of heterogeneous, high-performance, and adaptive computing in a digital world, characterised by a massive growth in data.

THE HINDU
INDIA'S NATIONAL NEWSPAPER SINCE 1878

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'PLI to offer more sops for IT hardware with IP designed in India'

The Hindu Bureau
HYDERABAD

India will soon unveil a production-linked incentive (PLI) scheme for IT server and hardware manufacturing, which would encourage use of intellectual property (IP) generated in the country, Union Minister of State for Electronics and Information Technology Rajeev Chandrasekhar said on Tuesday.

It would be similar to the PLI for mobile phones. "In the [new] PLI scheme, we will create additional incentives for manufacturers or OEMs that incorporate IP designed in India into their systems and products," he added.

The Minister, while virtually inaugurating the VLSI Design Conference 2023, also highlighted how India was pursuing a vision of emerging as a semiconductor hub, which, while being relevant to the country, would contribute to the global innovation ecosystem. "We are putting government capital to work in actively catalysing the India semiconductor ecosystem".

India aiming to achieve in 5 years what most nations did over decades in semiconductors: Min

In doing so, the Centre is working to achieve 'in five years what most countries, including China, attempted to do over several decades', he said.

Telangana Today

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Telangana Today

'PLI scheme for IT server soon'

STATE BUREAU
Hyderabad

The Central government will soon launch an information technology server and IT hardware production linked incentive (PLI) scheme. It will also offer additional incentives for manufacturers that incorporate Indian-designed intellectual property into their products, union Minister of State for Electronics and IT Rajeev Chandrasekhar said.

Virtually addressing VLSI Design Conference 2023 that began here on Tuesday, Chandrasekhar said the government has announced the Future Design Programme, which invests $200 million in startups that will design or co-design IP, tools or devices for the next generation of applications in India.

"By 2024, we believe India would have stepped into the semiconductor manufacturing space and catalysed a more domestic design and innovation ecosystem where we are encouraging startups to work with the global majors to develop IP, to develop devices either co-owned or owned for which the government has announced Future Design Programme," he said. The production linked incentive scheme for the IT hardware will be similar to the successful mobile phone PLI.

TS Principal Secretary (IT) Jayesh Ranjan said the State was at the forefront of startup revolution and had already created a wave of opportunities for innovation and startups across the technology sector and VLSI field, with leading semiconductor companies rapidly expanding in Hyderabad.

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Business Standard

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Will soon launch new PLI scheme for IT hardware: Union minister

The government will soon launch an information technology server and IT hardware production linked incentive (PLI) scheme, and also offer additional incentives for manufacturers that incorporate Indian-designed intellectual property into their products, Union Minister of State for Electronics and IT Rajeev Chandrasekhar said on Tuesday. Virtually addressing the VLSI Design Conference 2023, Chandrasekhar said the government has announced Future Design Programme, which invests $200 million in startups that will design or co-design IP, tools or devices for the next generation of applications in India. PTI

V6 NEWS వెలుగు
https://www.v6velugu.com https://epaper.v6velugu.com

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ఇన్నోవేషన్లకు ఇంపార్టెన్స్
ఐటీ రంగానికి పీఎల్ఐ స్కీమ్

- సెమీకండక్టర్ మిషన్ 10 బిలియన్ డాలర్లు
- వెల్లడించిన కేంద్రమంత్రి రాజీవ్ చంద్రశేఖర్

హైదరాబాద్ , వెలుగు: ఇన్నోవేషన్లను మరింత పెంచడానికి, టెక్నాలజీ ఎకోసిస్టమ్‌నుఇంకా విస్తరించడానికి మోడీ నాయకత్వంలోని ప్రభుత్వం అనేక చర్యలు తీసుకుంటున్నదని కేంద్ర ఎలక్ట్రానిక్స్ & ఐటీ, స్కిల్ డెవలప్‌మెంట్ & ఎంటర్‌ప్రెన్యూర్‌షిప్ శాఖల సహాయ మంత్రి రాజీవ్ చంద్రశేఖర్ అన్నారు. ఇందులో భాగంగా హార్డ్‌వేర్, సర్వర్‌ల కోసం ఐటీ రంగానికి పీఎల్ఐ పథకాన్ని త్వరలో ప్రారంభించనున్నట్లు మంత్రి తెలిపారు. ఈ పథకం తయారీదారులకు, ఒరిజినల్ ఎక్విప్‌మెంట్ తయారీదారులకు (ఓఈఎంలు) అదనపు ప్రోత్సాహకాలను అందజేస్తుందని చెప్పారు. ఇండియా సెమీకండక్టర్ మిషన్ కోసం కేంద్ర ప్రభుత్వం 10 బిలియన్ డాలర్లు ఖర్చు చేయనుందని చెప్పారు. దీనివల్ల మనదేశంలో సెమీకండక్టర్ ఇన్నోవేషన్ ఎకోసిస్టమ్ మరింత పెరుగుతుందని అన్నారు. అప్లికేషన్ల కోసం కో-డిజైన్‌ఐపీ, టూల్స్ తయారు చేసే స్టార్టప్‌లకు 200 మిలియన్ డాలర్లు ఇస్తామని వెల్లడించారు. వీఎల్ఎస్ఐ డిజైన్, ఎంబెడెడ్ సిస్టమ్స్‌పై హైదరాబాద్‌లో జరిగిన 22వ అంతర్జాతీయ సదస్సును ఉద్దేశించి ఆయన వర్చువల్‌గా మాట్లాడుతూ ఈ విషయాలు చెప్పారు.

సెమీకండక్టర్ పరిశ్రమకు ఎన్నో అవకాశాలు..

వీఎల్ఎస్ఐ డిజైన్ ఎంబెడెడ్ సిస్టమ్స్ రంగాలలో అవకాశాల గురించి మంత్రి మాట్లాడుతూ, "2014 కి ముందు, భారతదేశ డిజిటల్ ఆర్థిక వ్యవస్థ కొన్ని కంపెనీలు నిర్వహించే టెక్ సేవల పరిశ్రమకు మాత్రమే పరిమితమైంది. అయితే 2022లో డిజిటల్ టెక్నికల్ ఎకోసిస్టమ్ సిస్టమ్ గణనీయంగా పెరిగింది. ఎలక్ట్రానిక్స్ సెమీకండక్టర్ల రంగానికి మనదేశంలో అద్భుత అవకాశాలు ఉన్నాయి. ప్రపంచవ్యాప్తంగా పెరుగుతున్న డిజిటలైజేషన్‌తో ఉత్పత్తులకు డిమాండ్ పెరిగింది. ట్యాలెంట్‌కు గిరాకీ పెరిగింది. సెమీకండక్టర్ డిజైన్, మాన్యుఫ్యాక్చరింగ్ ప్యాకేజింగ్ ఎకోసిస్టమ్‌లో గ్లోబల్ స్టాండర్డ్స్‌ను అభివృద్ధి చేయడానికి భారతదేశం తీవ్రంగా కృషిచేస్తోంది. సెమికాన్ ఇండియా ఫ్యూచర్ డిజైన్ ప్రోగ్రాం కింద 2024 నాటికి దేశీయ స్టార్టప్‌లు గ్లోబల్ మేజర్‌లతో కలిసి పనిచేస్తాయి"అని మంత్రి వివరించారు. తెలంగాణ పరిశ్రమలు, ఐటీ సెక్రటరీ జయేష్ రంజన్ మాట్లాడుతూ సెమీకండక్టర్ల సెక్టార్‌సామర్థ్యాన్ని గ్రహించిన రాష్ట్ర ప్రభుత్వం సెమీకండక్టర్ అడ్వాన్స్‌డ్ కంప్యూటింగ్ రంగాలపై దృష్టి సారించి ఐటీ శాఖలో ప్రత్యేక విభాగాలను సృష్టించిందని అన్నారు. కార్యక్రమానికి దాదాపు 2,000 మందికి పైగా ఇంజనీర్లు, విద్యార్థులు, అధ్యాపకులు, పరిశ్రమ నిపుణులు, ప్రతినిధులు, విద్యాసంస్థలు, పరిశోధకులు, అధికారులు వచ్చారు. అంతర్జాతీయ వీఎల్ఎస్ఐ డిజైన్ & ఎంబెడెడ్ సిస్టమ్స్ కాన్ఫరెన్స్ వీఎల్ఎస్ఐ ఎంబెడెడ్ సిస్టమ్స్‌లోని తాజా పోకడలపై దృష్టి సారిస్తుంది.

पत्र नहीं मित्र
देशबन्धु
Influencing Public Opinion Since 1959

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केंद्र सरकार में प्रौद्योगिकी व नवाचार को मिला बढ़ावा : राजीव

वीएलएसआई डिजाइन के अंतरराष्ट्रीय सम्मेलन को किया संबोधित

नई दिल्ली, 10 जनवरी (देशबन्धु)। केंद्रीय इलेक्ट्रॉनिक्स और सूचना प्रौद्योगिकी और कौशल विकास और उद्यमिता राज्यमंत्री राजीव चंद्रशेखर ने मंगलवार को कहा कि प्रधानमंत्री नरेंद्र मोदी की अगुवाई में मौजूदा सरकार ने प्रौद्योगिकी और नवाचार के संपूर्ण क्षेत्र को प्रोत्साहन प्रदान किया है। उन्होंने कहा कि आठ साल पहले सिर्फ प्रौद्योगिकी का क्षेत्र सिर्फ सेवा तक सीमित तक लेकिन अब इसका परिदृश्य व्यापक हो गया है। उन्होंने कहा कि प्रधानमंत्री नरेंद्र मोदी की परिकल्पना के अनुसार, सरकार ने भारत के टेकेड के लक्ष्य को आगे बढ़ाने के लिए नवाचार और प्रौद्योगिकी पारिस्थितिकी तंत्र को फिर से तैयार करने और उसमें विविधता लाने के लिए कई उपाय किए हैं। केंद्रीय आईटी राज्यमंत्री हैदराबाद में आयोजित वीएलएसआई डिजाइन के 36वें अंतर्राष्ट्रीय सम्मेलन और एंबेडेड सिस्टम के 22वें अंतर्राष्ट्रीय सम्मेलन को वर्चुल माध्यम से संबोधित कर रहे थे, जिसमें 2,000 से अधिक इंजीनियरों, छात्रों, संकाय सदस्यों, उद्योग जगत के विशेषज्ञों और प्रतिनिधियों समेत शिक्षाविदों, शोधकर्ताओं, नौकरशाहों और सरकारी निकायों ने हिस्सा लिया। अंतरराष्ट्रीय वीएलएसआई डिजाइन और एंबेडेड सिस्टम्स सम्मेलन वीएलएसआई और एंबेडेड सिस्टम्स में नवीनतम प्रगति पर ध्यान केंद्रित करने वाला एक प्रमुख वैश्विक सम्मेलन है। वीएलएसआई डिजाइन और एम्बेडेड सिस्टम के क्षेत्रों में अवसरों पर चर्चा करते हुए उन्होंने कहा, ''2014 से पहले भारत की डिजिटल अर्थव्यवस्था कुछ कंपनियों द्वारा संचालित प्रौद्योगिकी सेवा उद्योग तक सीमित थी। हालांकि, 2022 में डिजिटल और प्रौद्योगिकी पारिस्थितिकी तंत्र को फिर से तैयार करके उसे विविधतापूर्ण बनाया गया जिसमें नवाचार और प्रौद्योगिकी के संपूर्ण क्षेत्र को शामिल किया गया है। भारत का टेकेड जितना महत्व इंटरनेट और एआई को दिया जा रहा है उतना ही इलेक्ट्रॉनिक्स और सेमीकंडक्टर्स को।

प्रौद्योगिकी पारिस्थितिकी तंत्र को फिर तैयार करने के लिए किए गए हैं कई उपाय : राजीव चंद्रशेखर

आईटी क्षेत्र में हार्डवेयर और सर्वर के लिए पीएलआई योजना जल्द ही शुरू की जाएगी

राज्यमंत्री ने कहा कि आईटी क्षेत्र में हार्डवेयर और सर्वर के लिए पीएलआई योजना जल्द ही शुरू की जाएगी। इस योजना से विनिर्माताओं और मूल उपकरण विनिर्माताओं ओईएम के लिए अतिरिक्त प्रोत्साहन मिलेगी जो भारत में डिजाइन किए गए आईपी को अपने सिस्टम और उत्पादों में शामिल करेंगे।

the pioneer

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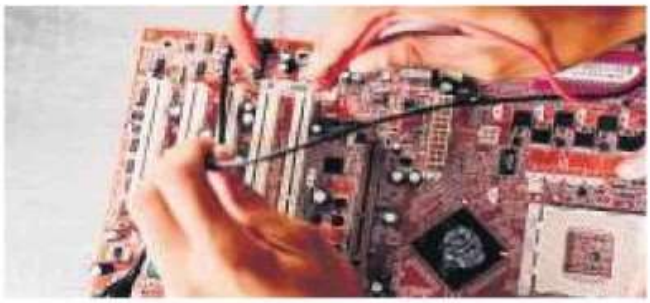
New PLI scheme for IT hardware soon: Union Minister

PNS ■ HYDERABAD



The Central government will soon launch an information technology server and IT hardware production linked incentive (PLI) scheme, and also offer additional incentives for manufacturers that incorporate Indian-designed intellectual property into their products, Union Minister of State for Electronics and IT Rajeev Chandrasekhar said on Tuesday.

Virtually addressing the VLSI Design Conference 2023 that began here today, Chandrasekhar said the government has announced Future Design Programme, which invests USD 200 million in startups that will design or co-design IP, tools or devices for the next generation of applications in India.

"By 2024, we believe India would have stepped into the semiconductor manufacturing space and catalysed a more domestic design and innovation ecosystem where we are encouraging startups to work with the leading global majors to develop IP, to develop devices either co-owned or owned for which the government has announced Future Design Programme," he said.

"To make sure that these devices and the innovation ecosystem is fully supported by the market, we will soon be launching an IT server and an IT hardware PLI that will be similar to the mobile phone PLI which has been extremely successful. In the IT PLI, we will create additional incentives for those manufacturers and original equipment manufacturers (OEMs) that incorporate Indian-designed IP into their systems, into their products," he said.

Chandrasekhar said, "We are putting government capital to work in actively catalysing the Indian semiconductor ecosystem and working with the industry and the global diaspora to ensure that our ambition of India as a semiconductor nation will be realised very rapidly".

The minister further said the Centre is also focused on Digital India RISC-V (DIR-V) programme for next generation microprocessors to achieve commercial silicon and design wins, according to a release by the organisers.

ఆంధ్రజ్యోతి

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ఐటీ హార్డ్‌వేర్, సర్వర్లకు త్వరలో పీఎల్‌ఐ

2024 నాటికి భారత్‌లో సెమీ కండక్టర్ల తయారీ

• చిప్ డిజైనింగ్‌లో స్టార్టప్‌లకు ప్రోత్సాహం • కేంద్ర ఎలక్ట్రానిక్స్, ఐటీ శాఖ సహాయ మంత్రి రాజీవ్ చంద్రశేఖర్

హైదరాబాద్ (ఆంధ్రజ్యోతి బిజినెస్): ఇన్ఫర్మేషన్ టెక్నాలజీ (ఐటీ) సర్వర్లు, హార్డ్‌వేర్ కోసం ఉత్పత్తి ఆధారిత ప్రోత్సాహకాల (పీఎల్‌ఐ) పథకాన్ని కేంద్ర ప్రభుత్వం ప్రకటించనుంది. అంతేకాక భారత్‌లో అభివృద్ధి చేసిన మేడ్ ఇన్ ఇండియా (ఐపీ) హక్కులు కలిగిన టెక్నాలజీలను తమ ఉత్పత్తుల్లో వినియోగించిన వారికి తయారీదారులకు అదనపు ప్రోత్సాహకాలను కూడా ఇవ్వనున్నట్లు కేంద్ర ఎలక్ట్రానిక్స్, ఇన్ఫర్మేషన్ టెక్నాలజీ శాఖ సహాయ మంత్రి రాజీవ్ చంద్రశేఖర్ తెలిపారు. హైదరాబాద్‌లో పీఎల్‌ఐను డిజైన్, ఎంబెడెడ్ సిస్టమ్స్‌పై అంతర్జాతీయ సదస్సులు ప్రారంభమైన సందర్భంగా ఆయన వర్చువల్‌గా మాట్లాడారు. ప్రభుత్వం హ్యూమన్ డిజైన్ ప్రోగ్రామ్‌ను ప్రకటించింది. ఈ ప్రోగ్రామ్ కింద ఏపీ టూల్స్, డిజైనెస్, భవిష్యత్ తరం అభివృద్ధిపథను డిజైన్, కోడిజైన్ చేసే స్టార్టప్ కంపెనీల్లో 30 కోట్ల డాలర్ల మేరకు పెట్టుబడులు పెట్టనున్నట్లు వెల్లడించారు. త్వరలోనే భారత్ ఎలక్ట్రానిక్స్ తయారీకి హబ్‌గా అవతరిస్తుందన్నారు. 2024 నాటికి భారత్ సెమీకండక్టర్ తయారీ రంగంలోకి అడుగు పెట్టనుందని తెలిపారు. ఇగా సెమీ కండక్టర్ పరిశ్రమకు 1,000 కోట్ల డాలర్ల (దాదాపు రూ.81,000 కోట్లు) ప్రోత్సాహకాలు ఇవ్వాలని ప్రభుత్వం భావిస్తోందన్నారు.

ప్రధాన కంపెనీలన్నీ హైదరాబాద్‌లోనే..

సెమీకండక్టర్ రంగంలో వస్తున్న అవకాశాలు అపారమైనవని, ఇప్పటికే ఇంటెల్, శ్యామ్, ఎఎండీ వంటి రంగాల్లో సముల మార్పులు తీసుకువస్తున్నాయని తెలంగాణ పరిశ్రమలు, ఐటీ శాఖ ప్రిన్సిపల్ కార్యదర్శి జయేశ్ రంజన్ అన్నారు. సెమీ కండక్టర్ రంగంలోని క్వాల్‌కామ్, ఇంటెల్ వంటి అంతర్జాతీయ కంపెనీలకు హైదరాబాద్‌లో కేంద్రాలు ఉన్నాయని, గత కొన్ని సంవత్సరాల్లో హైదరాబాద్ క్వాల్‌కామ్ కేంద్రంలో నిపుణుల సంఖ్య మూడు రెట్లు పెరిగిందని చెప్పారు. 2014 నుంచి సెమీ కండక్టర్ పరిశ్రమకు తెలంగాణ ప్రభుత్వం ప్రోత్సాహం అన్నివిధాల ఇస్తున్నట్లు చెప్పారు. ఈ రంగంలో నిపుణుల తయారీకి ప్రభుత్వం ప్రత్యేక శ్రద్ధ చూపుతోందని, పరిశ్రమకు అవసరమైన కోర్సులను ఆఫర్ చేస్తోందని అన్నారు.

దేశంలో ఏటా 2,800 కోట్ల డాలర్ల విలువైన సెమీ కండక్టర్లను వినియోగిస్తున్నారు. 2026 నాటికి ఇది 10,000 కోట్ల డాలర్లకు చేరుతుంది. ఇందులో కనీసం 20 శాతం సెమీకండక్టర్లను భారత్‌లోనే తయారు చేయాలని ప్రభుత్వం లక్ష్యంగా పెట్టుకుందని పీఎల్‌ఐను రూపొందించిన ఇండియా ప్రెసిడెంట్ పత్ర గుప్తా తెలిపారు.

ఏటా 2,800 కోట్ల డాలర్ల విలువైన సెమీ కండక్టర్ల వినియోగం..

ప్రస్తుతం అంతర్జాతీయ చిప్ కంపెనీలు 85 శాతం డిజైన్ పనులను భారత్‌లో చేస్తున్నాయి. తయారీని తైవాన్ వంటి దేశాల్లో చేస్తున్నాయి. దేశీయ కంపెనీలే మేడ్ ఇన్ ఇండియా హక్కులతో చిప్‌ను డిజైన్ చేయాలని అవసరం ఉందని చెప్పారు. చిప్ డిజైన్, తయారీ రెండింటిపై దృష్టి కేంద్రీకరించాల్సిన అవసరం ఉందన్నారు. ప్రస్తుతం దేశంలో 15 చిప్ డిజైన్ కంపెనీలు ఉన్నాయని, అవి ప్రధానంగా బెంగళూరు, హైదరాబాద్ నగరాల్లో ఉన్నాయని వివరించారు. వచ్చే పదేళ్లలో దేశంలో చిప్ డిజైన్ కంపెనీలు 10 రెట్లు పెరగాల్సిన అవసరం ఉంది. ప్రతి పట్టణంలో చిప్ డిజైనింగ్ కంపెనీ ఉండాలన్నారు. వేదాంత, ఐజీసీఎస్ వంటి కంపెనీలు ఫాబ్ యూనిట్లను దేశంలో పెట్టనున్నాయని చెప్పారు. ఫాబ్స్ మేడ్ సమస్త హక్కులు కలిగిన దేశీయ కంపెనీలు అవసరమని పేర్కొన్నారు.

BIZZ BUZZ

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Startups to pitch ideas at VLSI Design meet

Centre announced Future Design Programme to invest $200 million in startups that will design or co-design IP, tools or devices for the next generation of applications in India

BB Bureau
Hyderabad

STARTUPS Investor Connect will be the key highlight of VLSI Design Conference 2023 on January 2023, the last day of the event. The emerging startups in the very-large-scale integration (VLSI) and embedded sector will get an opportunity to pitch their ideas to investors. Anew Design Automation, Lightspeed Photonics, Vasbeam and Cyrrup are some of the startups participating in the programme.

Leading investors will be in attendance to hear the pitches and consider potential investments. The VLSI Design Conference is underway in Hyderabad from January 8, 2023 with a theme 'Semiconductors Driving Disruptive Innovations in Global Digitalization'. The five-day event is designed to bring together key stakeholders, industry professionals, academia, researchers, and government organisations.

Virtually addressing the attendees on the inaugural session on Tuesday, Rajeev Chandrasekhar, Minister of State for Electronics & IT and Skill Development & Entrepreneurship, said: "The Union government has announced Future Design Programme, which invests $200 million in startups that will design or co-design IP, tools or devices for the next generation of applications in India."

Jayesh Ranjan, Principal IT and (I&C) Secretary, Govt of Telangana, launches India's first 65 W GaN charger made by Powerics, partnered by Transphorm, Inc, MosChip & Silanna Semiconductors at the 36th International VLSI Design Conference in Hyderabad on Tuesday

The semiconductor industry is poised for monumental growth with the Centre having earmarked $10 bn for India Semiconductor Mission, creating an environment that encourages growth to create a vibrant semiconductor ecosystem and steer India into the future

The semiconductor industry is poised for monumental growth with the Centre having earmarked $10 billion for India Semiconductor Mission, creating an environment that encourages growth to create a vibrant semiconductor ecosystem and steer India into the future. He also highlighted the governments' partnerships with the stakeholders to realize its efforts towards actualising new milestones.

In his address, Jayesh Ranjan, Principal Secretary of Information Technology, Industry and Commerce for the Government of Telangana, said: "The State is at the forefront of the startup revolution. It has already created a wave of opportunities for innovation and startups across the IT sector and VLSI field, with leading semiconductor companies having their base, and rapidly expanding in Hyderabad."

Dr Satya Gupta, the President of VLSI Society of India highlighted the role of VLSI in developing and accelerating the pace of the sector in India and how it shaped the growth of startups. With scaling reaching its limits, it is time to look at addressing technological challenges at higher levels of abstraction in CMOS-based design and at the same time, look beyond Silicon for further performance enhancement.

BIZZ BUZZ

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New PLI scheme for IT hardware on anvil

Addl incentives for incorporating Indian-designed intellectual property into products: Union minister

HYDERABAD

THE Central government will soon launch an information technology server and IT hardware production linked incentive (PLI) scheme, and also offer additional incentives for manufacturers that incorporate Indian-designed intellectual property into their products, Union Minister of State for Electronics and IT Rajeev Chandrasekhar said on Tuesday.

Virtually addressing the VLSI Design Conference 2023 that began here, Chandrasekhar said the government has announced Future Design Programme, which invests $200 million in startups that will design or co-design IP, tools or devices for the next generation of applications in India.

"By 2024, we believe India would have stepped into the semiconductor manufacturing space and catalysed a more domestic design and innovation ecosystem

- Rajeev Chandrasekhar, MoS for IT and Electronics

"By 2024, we believe India would have stepped into the semiconductor manufacturing space and catalysed a more domestic design and innovation ecosystem where we are encouraging startups to work with the leading global majors to develop IP, to develop devices either co-owned or owned for which the government has announced Future Design Programme," he said.

FUTURE DESIGN PROGRAMME

- **Govt encouraging startups to work with the leading global majors**
- **Startups should develop devices either co-owned or owned, for which the govt announced Future Design Programme**
- **Future Design Programme has $200 mn for startups that will design devices for next generation applications**

"To make sure that these devices and the innovation ecosystem is fully supported by the market, we will soon be launching an IT server and an IT hardware PLI that will be similar to the mobile phone PLI which has been extremely successful. In the IT PLI, we will create additional incentives for those manufacturers and original equipment manufacturers (OEMs) that incorporate Indian-designed IP into their systems, into their products," he said.

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వచ్చే ఏడాదిలో దేశీయ సెమీకండక్టర్లు

- ఐటీ హార్డ్‌వేర్ తయారీకి పీఎల్‌ఐ
- కేంద్ర ఐటీ సహాయ మంత్రి రాజీవ్ చంద్రశేఖర్

వీఎల్‌ఎస్‌ఐ డిజైన్ సదస్సులో పాల్గొన్న మాస్‌చిప్ టెక్నాలజీస్ ఎండీ,సీఈఓ వెంకట సింహాద్రి, సత్యగుప్తా, జయా జగదీశ్, క్వాల్‌కాం కంట్రీహెడ్, వైస్ ప్రెసిడెంట్ శశిరెడ్డి, జయేశ్ రంజన్, ఏఎండీ అధ్యక్షుడు విక్టర్ పింగ్

ఈనాడు, హైదరాబాద్: మొబైల్ ఫోన్ తరహాలోనే ఇన్ఫర్మేషన్ టెక్నాలజీ సర్వర్, ఐటీ హార్డ్‌వేర్ తయారీకి ఉత్పత్తి అనుసంధాన ప్రోత్సాహకాల (పీఎల్‌ఐ)ను విస్తరింప జేస్తామని కేంద్ర ఐటీ, ఎలక్ట్రానిక్స్ సహాయ మంత్రి రాజీవ్ చంద్రశేఖర్ తెలిపారు. తమ ఉత్పత్తుల్లో భారతీయ అంకురాల మేధో సంపత్తిని (ఐపీ) చేర్చిన సంస్థలు, ఓఈఎంలకు అదనపు ప్రోత్సాహకాలూ ఇస్తామని తెలిపారు. మంగళవారం హైదరాబాద్‌లో నిర్వహించిన 36వ వీఎల్‌ఎస్‌ఐ డిజైన్ సదస్సు నుద్దేశించి, ఆయన దృశ్య మాధ్యమ పద్ధతిలో మాట్లాడారు. 2024లో దేశీయంగా సెమీకండక్టర్లు తయారు చేయాలని, డిజైనింగ్ - ఆవిష్కరణలు పెరగాలని ప్రభుత్వం ఆశిస్తోందని పేర్కొన్నారు. ఆధునిక సాంకేతిక పరికరాలు, ఐపీ, టూల్స్ డిజైనింగ్‌లోని అంకురాలకు తగిన ప్రోత్సాహం అందించేందుకు 200 మిలియన్ డాలర్లతో ఫ్యూచర్ డిజైన్ పథకాన్ని అందుబాటులోకి తెస్తున్నామని వివరించారు. దేశీయ అంకురాలు అంతర్జాతీయ సంస్థలతో కలిసి పనిచేసేలా ప్రోత్సహిస్తున్నట్లు తెలిపారు.

వీఎల్‌ఎస్‌ఐ రంగ సంస్థలకు ప్రోత్సాహకాలను అందిస్తున్నట్లు తెలంగాణ ఐటీ శాఖ ప్రిన్సిపల్ సెక్రటరీ జయేశ్ రంజన్ చెప్పారు. ఇంటెల్, క్వాల్‌కాం, మాస్‌చిప్ వంటి సంస్థలకు హైదరాబాద్ ఎంతో కీలకంగా ఉందన్నారు. పరిశ్రమకు కావాల్సిన నైపుణ్యాల కోసం విద్యార్థులకు శిక్షణ ఇస్తున్నట్లు తెలిపారు.

25 శాతం చేసినా..: 'దేశీయంగా సెమీ కండక్టర్ల విపణి ప్రస్తుత 28 బిలియన్ డాలర్ల నుంచి 2028 కల్లా 100 బి.డా.కు చేరుతుందని అంచనా. కానీ, దేశీయంగా ఇప్పటివరకు వీటి ఉత్పత్తి లేదు. భవిష్యత్ వినియోగంలో 25% ఇక్కడే ఉత్పత్తి చేయాలని ప్రభుత్వం ఆశిస్తోంది. దేశీయంగా చిప్ డిజైనింగ్ జరుగుతున్నా, ఉత్పత్తి ఇతర దేశాల్లో జరుగుతోంది. మనదేశం 5 లక్షల కోట్ల డాలర్ల ఆర్థిక వ్యవస్థగా మారే క్రమంలో సెమీకండక్టర్లు ఎంతో కీలకం. ఈ అవకాశాన్ని సంస్థలు అందుకోవాలి. దేశీయంగా సెమీకండక్టర్ల ఉత్పత్తికి 7 ప్రాంతీయ చాప్టర్లను ఏర్పాటు చేస్తున్నాం.'

- సత్య గుప్తా, ప్రెసిడెంట్, వీఎల్‌ఎస్‌ఐ సొసైటీ ఆఫ్ ఇండియా

ఇది ప్రారంభం మాత్రమే..: 'ఒకప్పుడు సెమీకండక్టర్లు, చిప్‌లను ఐటీలో భాగంగా చూసేవారు. ఇప్పుడు పరిస్థితి మారింది. ఈ రంగంలోకి విద్యార్థులు రావాలి. అంకురాలు ప్రారంభం కావాలి. ఫ్యాబ్‌లు ఏర్పాటవ్వాలి. డిజైనింగ్ మరింత అభివృద్ధి చెందాలి. నిపుణులకు కొదవ లేనందున దేశీయ అవసరాలను తీర్చడమే కాదు, ఎగుమతులూ చేయగలమనే విశ్వాసం ఉంది.'

- జయా జగదీశ్, కంట్రీహెడ్, సీనియర్ వైస్ ప్రెసిడెంట్, ఏఎండీ ఇండియా

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THE ECONOMIC TIMES

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PLI Sops for IT Hardware and Servers Soon

Our Bureau

New Delhi: The government will soon launch a new Production-linked Incentive (PLI) scheme for the production of IT servers and hardware, Minister of State for Electronics and IT Rajeev Chandrasekhar said.

The IT ministry will also offer additional incentives for companies that use and incorporate India-designed intellectual property in their products, Chandrasekhar added.

Addressing the VLSI Design Conference 2023, the minister said the government had also announced a Future Design Programme, which will invest $200 million in startups that design tools or devices in India.

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PLI scheme for IT hardware soon, says Chandrasekhar

PRESS TRUST OF INDIA
Hyderabad, January 10

THE CENTRAL GOVERNMENT will soon launch an information technology server and IT hardware production linked incentive (PLI) scheme, and also offer additional incentives for manufacturers that incorporate Indian-designed intellectual property into their products, Union minister of state for electronics and IT Rajeev Chandrasekhar said on Tuesday.

Virtually addressing the VLSI Design Conference 2023 that began here today, Chandrasekhar said the government has announced Future Design Programme, which invests $200 million in startups that will design or co-design IP, tools or devices for the next generation of applications in India.

"By 2024, we believe India would have stepped into the semiconductor manufacturing space and catalysed a more domestic design and innovation ecosystem where we are encouraging startups to work with the leading global majors to develop IP, to develop devices either co-owned or owned for which the government has announced Future Design Programme," he said. "To make sure that these devices and the innovation ecosystem is fully supported by the market, we will soon be launching an IT server and an IT hardware PLI that will be similar to the mobile phone PLI which has been extremely successful. In the IT PLI, we will create additional incentives for those manufacturers and original equipment manufacturers (OEMs) that incorporate Indian-designed IP into their systems, into their products," he said.

By 2024, we believe India would have stepped into the semiconductor manufacturing space and catalysed a more domestic design and innovation ecosystem

RAJEEV CHANDRASEKHAR
MoS, electronics and IT

Chandrasekhar said, "We are putting government capital to work in actively catalysing the Indian semiconductor ecosystem and working with the industry and the global diaspora to ensure that our ambition of India as a semiconductor nation will be realised very rapidly".

The minister further said the Centre is also focused on Digital India RISC-V (DIR-V) programme for next generation microprocessors to achieve commercial silicon and design wins, according to a release by the organisers.

The five-day conference themed "Semiconductors Driving Disruptive Innovations in Global Digitalisation" is designed to bring together key stakeholders, and represented by engineers, students and faculty, industry professionals, academia, researchers, bureaucrats and government organisations, it said.

Telangana government's principal secretary of information technology Jayesh Ranjan said the state is at the forefront of the startup revolution and has already created a wave of opportunities for innovation and startups across the technology sector and VLSI field, with leading semiconductor companies having their base, and rapidly expanding in Hyderabad.

Satya Gupta, president of VLSI Society of India, highlighted the role of VLSI in developing and accelerating the pace of the sector in India and how it shaped the growth of startups. Very-large-scale integration (VLSI) is the process of creating an integrated circuit (IC) by putting together thousands of transistors into a single semiconductor microchip.

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Govt will soon launch new PLI scheme for IT hardware: Min

PTI / Hyderabad

The Central government will soon launch an information technology server and IT hardware production linked incentive (PLI) scheme, and also offer additional incentives for manufacturers that incorporate Indian-designed intellectual property into their products, Union Minister of State for Electronics and IT ***Rajeev Chandrasekhar*** said on Tuesday.

Virtually addressing the VLSI Design Conference 2023 that began here today, Chandrasekhar said the government has announced Future Design Programme, which invests USD 200 million in startups that will design or co-design IP, tools or devices for the next generation of applications in India.

"By 2024, we believe India would have stepped into the semiconductor manufacturing space and catalysed a more domestic design and innovation ecosystem where we are encouraging startups to work with the leading global majors to develop IP, to develop devices either co-owned or owned for which the government has announced Future Design Programme," he said.

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Chandrasekhar said, "We are putting government capital to work in actively catalysing the Indian semiconductor ecosystem and working with the industry and the global diaspora to ensure that our ambition of India as a semiconductor nation will be realised very rapidly".

हिन्दुस्तान
तरक्की को चाहिए नया नजरिया

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आईटी हार्डवेयर के लिए पीएलआई जल्द

हैदराबाद, एजेंसी। इलेक्ट्रॉनिक्स एवं सूचना प्रौद्योगिकी राज्यमंत्री राजीव चंद्रशेखर ने मंगलवार को कहा कि केंद्र सरकार सूचना प्रौद्योगिकी सर्वर और आईटी हार्डवेयर के लिए ह्यउत्पादन से जुड़ी प्रोत्साहन योजना जल्द ही लेकर आने वाली है।

चंद्रशेखर ने यहां वीएलएसआई डिजाइन सम्मेलन 2023को ऑनलाइन संबोधित करते हुए कहा कि भारतीयों की विकसित बौद्धिक संपदा को अपने उत्पादों में आत्मसात करने वाले विनिर्माताओं के लिए भी सरकार अलग से प्रोत्साहन देगी।

राष्ट्रीय सहारा

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आईटी क्षेत्र के लिए PLI योजना जल्द

■ वर्ष 2024 तक सेमीकंडक्टर चिप का विनिर्माण करने लगेगा भारत

हैदराबाद (भाषा)।

इलेक्ट्रानिक्स एवं सूचना प्रौद्योगिकी राज्यमंत्री राजीव चंद्रशेखर ने मंगलवार को कहा कि केंद्र सरकार सूचना प्रौद्योगिकी सर्वर और आईटी हार्डवेयर के लिए 'उत्पादन से जुड़ी प्रोत्साहन' (पीएलआई) योजना जल्द ही लेकर आने वाली है।

चंद्रशेखर ने यहां आयोजित 'वीएलएसआई डिजाइन सम्मेलन 2023' को आनलाइन संबोधित करते हुए कहा कि भारतीयों की विकसित बौद्धिक संपदा को अपने उत्पादों में आत्मसात करने वाले विनिर्माताओं के लिए भी सरकार अलग से प्रोत्साहन देगी। उन्होंने कहा कि सरकार ने फ्यूचर डिजाइन कार्यक्रम की घोषणा की हुई है जिसमें स्टार्टअप कंपनियों में 20 करोड़ डालर के निवेश का प्रावधान है। भारत में अगली पीढ़ी के एप्लिकेशन के लिए बौद्धिक संपदा (आईपी), टूल्स या उपकरणों के डिजाइन में लगी स्टार्टअप कंपनियां इससे लाभान्वित होंगी।

चंद्रशेखर ने कहा, 'हमारा मत है कि वर्ष 2024 तक भारत सेमीकंडक्टर चिप के विनिर्माण में कदम रख चुका होगा जिससे अधिक नवाचारी पारिस्थितिकी एवं घरेलू डिजाइन को प्रोत्साहन मिलेगा। हम स्टार्टअप को वैश्विक दिग्गज कंपनियों के साथ मिलकर आईपी और उपकरणों के विकास के लिए बढ़ावा दे रहे हैं।' उन्होंने कहा कि इन उपकरणों और नवोन्मेषी पारिस्थितिकी को बाजार से पूरी तरह समर्थन सुनिश्चित करने के लिए सरकार जल्द ही आईटी सर्वर और आईटी हार्डवेयर के लिए एक पीएलआई योजना लेकर आएगी। यह मोबाइल फोन क्षेत्र की पीएलआई योजना की ही तर्ज पर होगी।

चंद्रशेखर ने कहा, 'आईटी क्षेत्र की पीएलआई योजना में हम उन विनिर्माताओं एवं मौलिक उपकरण विनिर्माताओं को अतिरिक्त प्रोत्साहन देंगे।

समाज्ञा
हिन्दी दैनिक
आपकी बात सच के साथ

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सरकार आईटी सर्वर, आईटी हार्डवेयर क्षेत्र के लिए लाएगी पीएलआई योजना: चंद्रशेखर

हैदराबाद : इलेक्ट्रॉनिक्स एवं सूचना प्रौद्योगिकी राज्यमंत्री राजीव चंद्रशेखर ने मंगलवार को कहा कि केंद्र सरकार सूचना प्रौद्योगिकी सर्वर और आईटी हार्डवेयर के लिए 'उत्पादन से जुड़ी प्रोत्साहन' (पीएलआई) योजना जल्द ही लेकर आने वाली है। चंद्रशेखर ने यहां आयोजित 'बीएलएसआई डिजाइन सम्मेलन 2023' को ऑनलाइन संबोधित करते हुए कहा कि भारतीयों की विकसित बौद्धिक संपदा को अपने उत्पादों में आत्मसात करने वाले विनिर्माताओं के लिए भी सरकार अलग से प्रोत्साहन देगी। उन्होंने कहा कि सरकार ने फ्यूचर डिजाइन कार्यक्रम की घोषणा की हुई है जिसमें स्टार्टअप कंपनियों में 20 करोड़ डॉलर के निवेश का प्रावधान है। भारत में अगली पीढ़ी के एप्लिकेशन के लिए बौद्धिक संपदा (आईपी), टूल्स या उपकरणों के डिजाइन में लगी स्टार्टअप कंपनियां इससे लाभान्वित होंगी।

The Telegraph

Edition: Print & Online | Page: National	Date: 10th January 2023

Startups to pitch ideas at VLSI Design meet

Startups Investor Connect will be the key highlight of VLSI Design Conference 2023 on January 2023, the last day of the event. The emerging startups in the very-large-scale integration (VLSI) and embedded sector will get an opportunity to pitch their ideas to investors. Anew Design Automation, Lightspeed Photonics, Vasbeam and Cyrrup are some of the startups participating in the programme

P3

the pioneer

Edition: Print & Online | Page: National	Date: 14th January 2023

VLSID CONFERENCE

Tie among two startups for Rs 50 cr fund

PNS ■ HYDERABAD

Two startups tied for the top spot at the start-up competition during 2023 Very large scale Integration (VLSI) design conference on Friday. Anscer Robotics and Lightspeed Photonics are the two companies that have tied for Rs 50 crore fund awarded by the conference. The competition featured a variety of innovative startups from around the world, all vying for the top spot.

Venkatesh Narasimhan, one of the conference organisers, said, "We wanted to have this

startup forum where we could identify startups, which are trying to do new chips, new subsystems of the chip, intellectual property components of a chip, innovative ways of connectivity, like photonics and embedded systems, where there is a hardware component to something new."

"At the Startup Forum - when we sent out the call for startups, we had 18 responses and we qualified them based upon some of the criteria that they should be involved in the design of either a chip, subsystem of a chip or an embedded system. Eventually, we shortlisted 12 startups for the investor pitch." Venkatesh added.

Anscer Robotics, an Indian-based company, creates autonomous mobile robots that are used in warehouses and other indoor environments to autonomously move various types of payload in an extremely cost-efficient way. Lightspeed Photonics, creates a free space optical interconnect that enables high-speed connections in high-performance computing systems at dramatically lower power consumption and high scalability.

Raghu Venkatesh, co founder, Anscer Robotics, said, "We aimed to create a multinational company when we started. Robotics technology is not very popular in Indian market. So we took an opportunity and launched our company, we started in 2020 during pandemic with a very talented team. Then we applied for T-hub semiconductor programme and with the investments, we are foreseeing a billion dollar company in the next few years."

www.enrightpr.in

Reader & Viewers: International	Page: World Wide	As on 14th January 2023

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City to host VLSI Design Conference

The Hindu Bureau
HYDERABAD

Hyderabad will play host to the VLSI Design Conference 2023 in January, a sought after event for stakeholders in the semiconductor, VLSI and Embedded systems space. Comprising the 36th International Conference of VLSI Design and the 22nd International Conference on Embedded Systems, the event will be held from January 8-12 with the support of the Telangana government, the organisers of VLSI Society of India said on Thursday.

More than 2,000 global delegates are expected to attend the 5-day conference that will also feature around 70 promising IEEE publications (papers and posters). Union Minister of State for Electronics and Information Technology Rajeev Chandrasekhar, Telangana Industries and IT Minister K.T. Rama Rao and eminent industry leaders are scheduled to speak at the inaugural session of the programme consisting of two-day tutorials and three-day conference. The tutorials will witness industry and academia footfall with sessions running in three parallel tracks, while the conference will have address by VLSI industry leaders, academia and scientific researchers. An Industry Forum with sessions on 5G, Automotive, Semiconductor Manufacturing and Outsourced Semiconductor Assembly and Test will be the highlight of the event. A host of contests such as Design Contest, User Design Track, and Student Research Forum for budding VLSI engineers, will be part of the conference, the organisers of the event added. The first physical conference post-pandemic, it has garnered a lot of interest and enthusiasm and set to emerge as a platform for industry and academia to discuss, deliberate, and explore the frontiers of the semiconductor ecosystem, said General Chair, VLSID 2023, Sanjay Churiwala.

HYD TO HOST VLSI DESIGN MEET IN JAN.

DC CORRESPONDENT
HYDERABAD, DEC. 1

The 36th International Conference of VLSI Design and the 22nd International Conference on Embedded Systems will be held in Hyderabad from January 8 to 12 next.

More than 2,000 global delegates are expected to attend the five-day VLSI Design Conference, which is being backed by the Telangana government.

The conference will host more than 70 promising IEEE publications, with the best among them getting recognised under the conference's flagship award categories.

Social Media Buzz

TimesTech Buzz @TimestechBuzz · Jan 11

The @VLSIDConf2023 started with motivating speeches from #industry #leaders. Additionally, the **conference** also discussed the newest advancements in **VLSI**, embedded technologies and #opportunities for #innovation. #electronicsnews #technologynews

timestech.in
VLSI Design Conference Begins with Dialogues on Innovations
The VLSI Design Conference 2023 started with motivating speeches from industry leaders. Additionally, the conference also discussed

India Education Diary @indiaedudiary · Jan 11

Minister Rajeev Chandrasekhar graces 36th International **conference** on **VLSI** Design and 22nd International **conference** on Embedded Systems

indiaeducationdiary.in
Minister Rajeev Chandrasekhar graces 36th International conference ...
New Delhi: The Minister of State for Electronics & Information Technology and Skill Development & Entrepreneurship, Shri Rajeev ...

Odisha Diary @OdishaDiary · Jan 10

MoS Shri Rajeev Chandrasekhar addresses 36th International **conference** on **VLSI** Design and 22nd International **conference** on Embedded Systems

orissadiary.com
MoS Shri Rajeev Chandrasekhar addresses 36th International confere...
The Minister of State for Electronics & Information Technology and Skill Development & Entrepreneurship, Shri Rajeev Chandrasekhar today ...

Electronics Era @era_electronics · Jan 12

Inaugural Ceremony of the #VLSIDesignConference2023 #technologynews #techupdate #technology #electronics #electronicsnews #design

electronicsera.in
Inaugural Ceremony of the VLSI Design Conference 2023
The VLSI Design Conference 2023 got off to a phenomenal start today with motivating speeches from industry leaders and discussions

businessline @businessline · Jan 10

After launching #PLI (Production Linked Incentives) for several sectors, the Union Government has announced that it is going to launch an exclusive PLI for #IT Servers and IT Hardware.

thehindubusinessline.com
PLI for IT servers and hardware on the anvil: Rajeev Chandrasekhar
To offer more sops for makers incorporating India-designed IPs; country will step into semicon manufacturing space in 2024

thenewsfactsnow @thenewsfactsnow · 7h

.@VLSIDConf2023 VLSI Design Forecast: Advancements in EV and 5G to Drive Growth: Experts
The 3-day meet, was a huge success with over 1600 registrations, Impacting Keynotes,63 booths from 43 companies, Promising Startups
#VLSID2023 #Semiconductor

thenewsfacts.com
2023 VLSI Forecast: Advancements in EV and 5G to Drive Growth: Ex...
The three-day 2023 VLSI Design Conference, which focused on the semiconductor industry, was a huge success with over 1600 ...

Sheela Panicker @SheelaPanicker · 23h

#Startup pitch competition ends n tie @anscerrobotics @LSPhotonics take the Top Spot for Rs.50 Cr funding @VLSIDConf2023 @startuphyd #Hyderabad #vlsidesign #embeddedsystems #conference #Robotics

startuphyderabad.com
Startup Competition Ends in Tie for First Place: An...
The startup competition at the 2023 VLSI Design Conference concluded on Friday evening in ...

startupstars India @Startupstars_IN · 23h

VLSID 2023 Startups Challenge: Anscer Robotics and Lightspeed Photonics Crowned Winners @VLSIDConf2023 startups competition #VLSID2023 #StartupsContest #VLSIDesignConference

startupstars.in
VLSID 2023 Startups Challenge: Anscer Robotics and Lightspeed Pho...
Hyderabad, India - VLSID 2023 Startups Competition concluded on Friday evening in Hyderabad with a tie for first place between Anscer ...

Sheela Panicker @SheelaPanicker · Jan 10

.@VLSIDConf202 started off @jayesh_ranjan addressing a packed house, said #Telangana is realizing the growth potential has created separate wings in the IT department focused on semiconductor and advanced computing areas #Hyderabad

thehindu.com/news/cities/Hy...

The Hindu Business @thehindubiz · Jan 11

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indiaeducationdiary.in/mos-shri-rajee...

SUMMARY

700M+ OVERALL REACH

(ONLINE + PRINT+TV)

100M+ Online ARTICLES	100M+ PRINT Coverage	150M+ MENTIONS

350M+ ONLINE News Reach	10M+ PRINT Reach

The agency has earned excellent editorial space for

VLSI Conference 2023

AD VALUE EQUIVALENT TO

6.5M USD

Thank
you